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7217/71824

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Takatomo Nagamine et al.

Serial No.

10/511,733 -

Filed

October 18, 2004

For

VIDEO SIGNAL PROCESSING CIRCUIT AND VIDEO

SIGNAL PROCESSING METHOD

Group A.U.

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Examiner

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I hereby certify that this paper is being deposited this date with the U.S. Postal Service as first class mail addressed to: Commissioner for Patents,

PO Box 1450

Alexandria, VA 22313-1450

Jay H. Maioli

Reg. No. 27,213

Date March 9, 2005

> March 9, 2005 1185 Avenue of the Americas New York, NY 10036 (212) 278-0400

## <u>UNDER 37 CFR § 1.97(c)</u>

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

As a means of complying with the duty of disclosure set forth in 37 CFR § 1.53 and in keeping with the guidelines of 37 CFR 1.98, Applicants hereby submit information thought to be relevant to the examination of the above-identified

application, Also submitted herewith is a completed form PTO-1449.

-No first office action has been received in this application.

Japanese Patent Application 2001-095005, relates to a clock-generating circuit capable of structuring YC separation and chroma demodulation of NTSC and PAL systems, etc., without greatly changing a sampling clock.

Japanese Patent Application 2000-013813, relates to a video signal processing circuit that uses a video signal switching circuit which outputs composite video signals via an output terminal used for normal composite video signals. Another output terminal outputs the color signal when an Sinput signal is inputted.

Japanese Patent Application 2000-253417, relates to a digital ACC circuit capable of executing ACC operation even when the amplitude level of an input digital video signal is low due to weak electric field or the like.

Respectfully submitted,

COOPER & DUNHAM LLP

Maioli

Reg. No. 27,213

JHM/JBG Encl.

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RADENA Commerce
Patent and Sheet 1 of 1 U.S. Department of Atty. Docket No. Serial No. 7217/71824 10/511,733 Patent and Trademark Office Applicants Takatomo Nagamine et al. INFORMATION DISCLOSURE CITATION Filing Date Group (Use several sheets if necessary) October 18, 2004 U.S. PATENT DOCUMENTS Document Number Examiner Date Class Subclass Filing Date Name Initial if Appropriate FOREIGN PATENT DOCUMENTS **Document Number Country Class Subclass Date** Translation Yes No 2001-095005 JP 4/6/01 H04N 9 [abstract] Japan 66 JP 2000-013813 1/14/00 H04N 9 64 Japan [abstract] JP 2000-253417 9/14/00 Japan 68 H04N 9 [abstract] OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	DATE CONSIDERED	

<sup>\*</sup>EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.